Nottingham Trent University

School of Science and Technology

A New Form of Educational Logic Gate Simulator

by

Elliot Christopher Harding

in

2020

Project report in part fulfilment

of the requirements for the degree of

Bachelor of Science with Honours

in

Software Engineering

I hereby declare that I am the sole author of this report. I authorize the Nottingham Trent University to lend this report to other institutions or individuals for the purpose of scholarly research.

I also authorize the Nottingham Trent University to reproduce this report by photocopying or by other means, in total or in part, at the request of other institutions or individuals for the purpose of scholarly research.

Signature

Elliot Christopher Harding

Abstract

This work introduces the use of logic gate simulators in educating students about the functionality of logic gates and how they come together to form circuits. It introduces existing methods on how this is accomplished. The report then goes on to investigate an apparently new design of logic gate simulator intended to educate students on logic gates, to an A-Level standard, more effectively. The new design, with its implementation described within the report, involves a challenge mode, in which teachers can create truth table to circuit and circuit to truth table conversion-based challenges for students. The challenge-based approach is intended to engage students in the ‘learning through reflection on doing’ process. This approach through means of user testing and observation has been deemed successful in that it provides students an alternative method to learn about logic gates, and evidence in this report shows that it may be more effective than previous approaches.

Acknowledgements

Enter acknowledgements here. It is usual to acknowledge those that have assisted you in your work and will normally include your main project supervisor. The order of acknowledgments (most important first) and their respective length indicates their relative importance to you.

Neil Sculthorpe ~ Tutor, review points, provided information relevant to report, overlooked project planning document ect…

Ferreira, Joao Filipe ~ Helped with formulating idea for new approach to logic gate simulator education

Table of Contents

[Abstract ii](#_Toc37356691)

[Acknowledgements iv](#_Toc37356692)

[Table of Contents v](#_Toc37356693)

[List of Figures ix](#_Toc37356694)

[List of Tables x](#_Toc37356695)

[CHAPTER 1 1](#_Toc37356696)

[Introduction 1](#_Toc37356697)

[1.1 Introduction 1](#_Toc37356698)

[1.1.1 Scope 1](#_Toc37356699)

[Yes 1](#_Toc37356700)

[1.2 Background 1](#_Toc37356701)

[1.2.1 Logic Gate Simulators 1](#_Toc37356702)

[1.2.2 Education and Learning Processes 1](#_Toc37356703)

[1.2.3 Use of Logic Gate Simulators for Education 1](#_Toc37356704)

[1.3 Issues with Using Logic Gate Simulators for Education 2](#_Toc37356705)

[1.4 Goal of Report 2](#_Toc37356706)

[1.5 Prepare Your Report 3](#_Toc37356707)

[1.6 Heading Styles 4](#_Toc37356708)

[1.6.1 Numbering of Headings 4](#_Toc37356709)

[1.7 Document Paragraphs 4](#_Toc37356710)

[1.7.1 First Paragraph Following a Heading 4](#_Toc37356711)

[CHAPTER 2 5](#_Toc37356712)

[CONTEXT 5](#_Toc37356713)

[2.1 Introduction 5](#_Toc37356714)

[Purpose of context… avoid accidentally repeating a technique tried before 5](#_Toc37356715)

[2.2 Existing Research 5](#_Toc37356716)

[2.3 Existing Solutions 5](#_Toc37356717)

[2.3.1 Solution 1 5](#_Toc37356718)

[2.3.2 Solution 2 5](#_Toc37356719)

[2.4 Insert Pictures 6](#_Toc37356720)

[2.5 Insert Tables 6](#_Toc37356721)

[CHAPTER 3 8](#_Toc37356722)

[New Ideas 8](#_Toc37356723)

[3.1 Introduction 8](#_Toc37356724)

[So pretty much new idea, justify it, and how it will be approached 8](#_Toc37356725)

[3.2 Requirements 8](#_Toc37356726)

[3.3 Project Planning 8](#_Toc37356727)

[3.3.1 Methodology Used 8](#_Toc37356728)

[3.3.2 Objectives and Milestones 8](#_Toc37356729)

[3.3.3 Workload Balancing 8](#_Toc37356730)

[3.3.4 Contingency Planning 8](#_Toc37356731)

[CHAPTER 4 10](#_Toc37356732)

[IMPLEMENTATION 10](#_Toc37356733)

[4.1 Introduction 10](#_Toc37356734)

[4.2 Why GitHub 10](#_Toc37356735)

[4.3 Base Simulator Design 10](#_Toc37356736)

[4.3.1 Why C++ 10](#_Toc37356737)

[4.3.2 Why QT 10](#_Toc37356738)

[4.3.3 GUI 10](#_Toc37356739)

[4.3.4 Code Core Design 10](#_Toc37356740)

[4.3.5 Features 11](#_Toc37356741)

[4.4 Task Feature 11](#_Toc37356742)

[4.4.1 Development 11](#_Toc37356743)

[4.4.2 Result 11](#_Toc37356744)

[4.5 Unit Testing 11](#_Toc37356745)

[4.5.1 Benefits of Unit Testing 11](#_Toc37356746)

[4.5.2 Why QTest 11](#_Toc37356747)

[CHAPTER 5 13](#_Toc37356748)

[RESULTS / DISCUSSION 13](#_Toc37356749)

[5.1 Introduction 13](#_Toc37356750)

[CHAPTER 6 14](#_Toc37356751)

[CONCLUSIONS / FUTURE WORK 14](#_Toc37356752)

[6.1 Conclusions 14](#_Toc37356753)

[6.2 Future work 14](#_Toc37356754)

[6.3 Legal, Social, Ethical and Professional Issues 14](#_Toc37356755)

[6.4 Synoptic Reflections 16](#_Toc37356756)

[ReferenceS 17](#_Toc37356757)

[Bibliography 18](#_Toc37356758)

[Appendix A 19](#_Toc37356759)

**Note:** To insert TOC here follow the instruction below. You may also right click on the above text and use “Update Field” to update the TOC.

Insert > Reference > Index and Tables … > Table of Contents

List of Figures

[Figure 1: Microsoft XP 5](#_Toc125880397)

**Note:** To insert List of Figures here follow the instruction below. You may also right click on the above text and use “Update Field” to update the list of figures.

Insert > Reference > Index and Tables … > Table of Figures > Caption Label => Figure

List of Tables

[Table 1: Microsoft Office. 5](#_Toc125880862)

**Note:** To insert List of Tables here follow the instruction below. You may also right click on the above text and use “Update Field” to update the list of tables.

Insert > Reference > Index and Tables … > Table of Figures > Caption Label => Table



Introduction

Introduction

### Scope

This report will focus as discussed in the abstract on a new design of logic gate simulator for educating A-Level students on the functions of logic gates. The new piece of software intends to target the issue discussed below and as a result provide a better means of educating students.

In order to get a better understanding of the topic at hand this report will delve into the general subject area surrounding logic gates and their education. This involves the areas of Computer Science, education techniques and various methods of educating students on logic gates.

## Background

Logic circuits are a fundamental part of all computer science related education programs. An understanding of these gates, how they come together to form circuits, and how these circuits are used is necessary for the successful study and implementation of the technological systems from which they are composed.

### Logic Gate Simulators

Logic gate simulators attempt to simulate logic gates and their behaviour when combined into circuits. They achieve this at varying levels of physical detail, such as at the transistor, gate, electronic system, or behavioural levels.

The primary use case for these simulators, especially those that are more complex, is for circuit design verification. By allowing users to directly interact with their designed model, they can see it in action without having to physically build it themselves. This dramatically reduces development costs as circuits only need to be built once users are assured the logic behind the circuits is functional to their requirements.

(Maybe give some example of logic gate simulators here)

### Use of Logic Gate Simulators for Education

Using software as a teaching method.

Logic gate simulators can be used as an education tool to teach students the behaviour of gates and how they come together to form circuits. This report shall focus on logic gate education for A-Level students. From this perspective, features are usually from the gate/transistor level upwards.

Most of these tools make use of the experiential learning process, defined as ‘Learning through reflection on doing’ [1]. Various studies [2][3] demonstrate the effectiveness of the experiential learning process. These methods will be discussed further in detail within the context section of the report.

1.2.3.1 Recent Work

## Issues with Using Logic Gate Simulators for Education

Without a challenge or problem to overcome, getting students engaged in the learning process can prove difficult. Current logic gate education software tools fail to use challenge-based learning to achieve their goal. This is unfortunate, since challenge-based learning, based off-of experiential learning, is proven to be effective in engaging students in the learning process [4][5].

## Report Layout

A literature review will be performed within the context section of this report. Here, various other methods of logic gate education will be looked at. This review avoids accidentally repeating a technique that has been tried before ensuring that the method is entirely unique. The creation of this section will also aid in shaping the method and ensuring its validity.

The new ideas section of the document will outline the planning of the implementation of this new piece of software. This will outline the various stages of implementation and introduce some of the tools used in the process.

The design of the software will then be discussed within the implementation section. Here, the structure of the design will be explained so that readers can replicate the work. How the tools are used for the software will also be discussed here along with a display of the product itself.

Results and the overall success of the software on tackling the goal it set out to overcome will be discussed within the discussion section of the report. Finally, the report will be summarised within the conclusion section of the report.

This chapter is the introduction to the main text and is intended to describe the background of the work, state the reasons for the investigation and what benefits will result in the long term. You should repeat, and briefly expand on, the points made in the abstract. Bear in mind this is the 'INTRODUCTION' to the entire project report and NOT just an introduction to the general subject area of your project. As such it should touch on all aspects of the following chapters. It is a guide to what follows in your project report in much the same way as the abstract is a VERY short description of the work. It should include an indication of the contents of the various chapters of the report.

Do not repeat large pieces of standard texts or theory here. You may well have done work in an area that is novel to you and think that a lengthy explanation in your own words will show that you now understand the area. This is a very common fault in student reports so do try to avoid falling into the same trap as your predecessors and probably peers too. A simple reference to a standard text will suffice. If necessary the reader can then go and read the standard text on the subject.

This chapter should include some historical details (most likely from standard text books on the subject) and a brief overview of recent work in the subject area.

This chapter should also include the intended scope of the project and, most importantly, set it in context. That is you should make clear the intended benefits to general computing and those who practise it.

Prepare Your Report

Use this document and its formatting as the template for your Project report.

Typing must be on A4 paper, Verdana font size 10, Double Line Spacing, on one side of the paper only and with margins of 2.5cm at top, right and bottom edges and 4cm at the left edge. Pages must be numbered at centre bottom according to the conventions of 9.4. That is Abstract, Acknowledgements, Contents and List of Figures are numbered in consecutive lowercase Roman numbers. The pages bearing the main body of the report, i.e.: the 6 Chapters, are numbered consecutively, (1, 2, 3, ..,6) followed by the References and Bibliography. The Appendices may be numbered following on after the main body or, more usually, have the Appendix identifying letter followed by page number within Appendix. (i.e.: Appendix A is numbered A1, A2, A3, A4..., Appendix B is numbered B1, B2, B3, B4....).

Use Style and formatting options to edit your document. Please use only the following Styles in your report.

* Section Heading 1 – Verdana Font, 14 pt, Bold
* Section Heading 2 - Verdana Font, 13 pt, Bold
* Section Heading 3 - Verdana Font, 12 pt, Bold
* Section Heading 4 - Verdana Font, 12 pt
* Abstract - Verdana, 10 pt, 1.5 Line Spacing
* Normal – Verdana, 10 pt, Double Line Spacing
* Bulleted - Verdana, 10 pt, , 1.5 Line Spacing
* References and Bibliography - Verdana, 10 pt, 1.5 Line Spacing
* Figure and Table Caption - Verdana, 10 pt, Bold

Heading Styles

For different level of section heading use an appropriate style and the numbering of the section will be done automatically. Four levels of section headings have been defined, Section Heading 1 through Section Heading 4.

The headings have been defined in the Verdana font, and all are aligned on the left. Varying font sizes and styles have been defined. The alignment and the font sizes and weights can all be changed by doing a Format, Style, selecting the desired style, and making any necessary modifications.

Numbering of Headings

This template numbers the headings in a legal numbering format. It is recommended to use the same style even if you are not using this template.

Document Paragraphs

First Paragraph Following a Heading

All headings will be followed automatically by a paragraph with a style called Normal. Normal paragraphs do not have an indent on the first line.



CONTEXT

Introduction

The purpose of this section of the report is to review work within the general field of logic gate education. This avoids accidentally repeating a technique that has been tried before ensuring that the new method is entirely unique. The creation of this section will also aid in shaping the method and ensuring its validity.

## Existing Research

Mention limitations of research, which is where you build off for your solution

## Existing Solutions

Teaching using models:

<https://www.researchgate.net/publication/305751603_Teaching_Digital_Logic_Circuit_Design_via_Experiment-Based_Learning_-_Print_your_own_Logic_Circuit>

Simulator software:

<https://www.researchgate.net/publication/309907692_Using_Logisim_Educational_Software_in_Learning_Digital_Circuits_Design>

<https://www.tandfonline.com/doi/abs/10.1080/03043797.2016.1196344?scroll=top&needAccess=true&journalCode=ceee20>

Interactive web circuit simulator:

<https://www.researchgate.net/publication/318361261_WEB_Based_Interactive_Digital_Logic_Circuit_Simulator>

Mobile education:

<https://www.researchgate.net/publication/320468266_Development_of_A_Mobile_Learning_for_Digital_Logic_Gates_in_Vocational_Schools>

### Solution 1

Mention limitations of research, which is where you build off for your solution

### Solution 2

This should include a literature review in order to detail the State-of-the-Art in the field and the main areas for improvement/further research. One of the main reasons for the literature review is to avoid accidentally repeating a technique that has been tried before. In your literature survey, do be precise about the volume numbers of journals and exact ranges of abstracts you have searched. These details must be precise enough for anyone following up your work to avoid searching the same material. If searching computer data bases, including the CD-ROM data bases available in the library, then give precise details of the search keys used. Perhaps a printed copy of the session/s as an appendix might show this clearly. A new idea for a project is usually not entirely new. The project may try to apply an existing technology in a new area or for a different type of customer, or enhance or specialize the current functionality of the technology.

As a result of your literature review you should be able to elaborate on the limitations of existing methods of solution for your particular problem.

Insert Pictures

Insert a picture.



Figure : Microsoft XP

To generate a List of Figures or List of Tables, select the text under the appropriate heading, and do an Insert, Index and Tables.

Insert Tables

Insert tables

Table : Microsoft Office.

|  |  |  |  |
| --- | --- | --- | --- |
| Col 1 | Col 2 | Col 3 | Col 4 |
| 12.00 | 13.00 | 15.00 | 17.00 |
| 22.00 | 11.00 | 19.00 | 12.00 |



New Ideas

Introduction

### So pretty much new idea, justify it, and how it will be approached

## Requirements

## Project Planning

Talk about ISO 1962

### Methodology Used

### Objectives and Milestones

Put in milestone table

### Workload Balancing

Gantt Chart

### Contingency Planning

As a result of your 'Context' chapter you should have narrowed down your area of research. This 'focussing' of attention on one aspect of the field will have been aided by reading about other peoples' work in the field. You may be proposing a development of one of their ideas or perhaps an idea that came to you that differs from anything tried before.

For a software development you might include an explicit list of the requirements, a description of investigation of requirements ( if appropriate), and a discussion of how requirements relate to Background research.

For a research-based investigation you might include the planning for the process (methodology) to be adopted, the criteria to be used for evaluation, and a discussion of reasons for this process and comparison with alternatives.

The proposed development or investigation must be realistic bearing in mind the entire project is supposed to take 400 hours of your time. Thus, evidence of project planning must be included in this chapter; estimates of work load for the various phases, setting these in context with other estimated workloads (e.g. course work and revision) and other deadlines. This should allow you to establish your project timetable (perhaps in the form of a Gantt chart) showing the interaction of these various factors and the set objectives/milestones. In your planning you should include contingency planning to allow for the unexpected disaster. Various project planning tools are covered in the course to allow you to do this.

Discussion of platform of software. ?

JUSTIFY NEW IDEA



IMPLEMENTATION

Introduction

## Why GitHub

## Base Simulator Design

### Why C++

Tool used (ref), alernative tools (ref), why tool was chosen

### Why QT

Tool used (ref), alernative tools (ref), why tool was chosen

### GUI

4.3.3.1 Images

Tool used (ref), alernative tools (ref), why tool was chosen

### Code Core Design

4.3.4.1 Methods of Simulator Design

### Features

## Task Feature

### Development

### Result

## Unit Testing

### Benefits of Unit Testing

### Why QTest

Tool used (ref), alernative tools (ref), why tool was chosen

Here you give details of the development or investigation of the new material proposed in 'New Ideas'. This must be done in a business-like manner. The development of any software must follow a suitable analysis and design methodology. There are CASE tools available to you for some methodologies, others will have to be a 'paper' design. An investigation must also follow a suitable methodology and use appropriate techniques and tools.

Software-based projects, requiring the production of a software solution for a set of requirements, should demonstrate that the software development has undergone appropriate analysis, design, project management, structured programming and testing. Research-based projects, requiring an investigation of a research question or client’s requirements, or being used to test a hypothesis, should demonstrate that the investigation has been properly conducted, is based on scientific principles and uses appropriate tools, techniques and standards. An investigation must produce a technical outcome from some development (software or hardware (e.g. networks, displays)) or testing (e.g. of system/network performance, system security, HCI/usability analysis). Sometimes a software prototype or a testing framework will be produced for the evaluation or testing of the research or hypothesis. Work based purely on literature review is not acceptable.

Some projects aim to provide software for general use as their final product and these must include relevant aspects of HCI (Human Computer Interaction) and address such features of usability such as 'user friendliness' and most likely employ GUI (graphical user interface) standards such as Windows.

In any case, students often ask what should go in this chapter, how to describe what they have done, what is relevant, how much of existing work to include, what to include from what they have done, etc. The simplest and surest way is to refer to your diary of the work you have done and report on it in chronological order.

The complete requirements analysis, problem analysis & design of software must be done rigorously and included in full in an appendix. Avoid cross-referencing it too often, thus causing the reader to keep flicking pages back and forth, rather reproduce sections that you wish to draw the reader's attention to. That is, highlight the parts that you found particularly difficult to implement and feel rather proud of having solved. Do not include lengthy descriptions of standard techniques or methodologies, simply state that 'such-and-such was designed using such-and-such technique (give a reference, not just 'SSADM' but 'SSADM [James 1996]' where the reference is a standard text on the technique!)' and highlight where you found shortcomings in the technique that didn't quite cope with your particular problem. Highlight exceptions to the standard.



RESULTS / DISCUSSION

Introduction

The technique developed in your project is supposed to show improvement on techniques previously available. Therefore it may be necessary to spend time investigating whether this is true. Perhaps you need to set up some sort of quantitative test and do a little statistical analysis to confirm the improvement. This chapter will provide evidence, from the tests that you carry out, of the outcomes of your project.

Explain the success and limitations of your work and show how this relates to the aims and objectives set out in the introduction.



CONCLUSIONS / FUTURE WORK

## Conclusions

Whatever it was that your results showed should be summarised here. Your project or may or may not have achieved all that you set out to at the start.

This is your opportunity to conclude whether the project was a ‘success’ and how it might have been tackled differently in hindsight.

## Future work

In either case there should be some reference to future work, either to forward and expand on the successful outcome or to test ways of overcoming the shortfall in your ideas that didn't work out quite as expected but there should be something that shows you can see further implications of what you have achieved.

## Legal, Social, Ethical and Professional Issues

This section should include a discussion of the four LESPIs and the way in which you project has/will/could impact on each.

* Describe the four LESPI’s

Legal issues could be improved by considering relevant legislation, e.g. GDPR, Accessibility Legislation. Some points would benefit from more discussion, e.g. your intent to release the software as open-source (why are you doing this, what implications could it have?). For professional issues, you could look at a Code of Conduct from a professional body, e.g. BCS, and see if anything is relevant to the project.

During the research phase of the project, students and teachers will be interviewed on the features of logic gate simulators. In order to ensure this does not raise any LSEPIs, each interviewee with be given a participation consent form which they will sign. The form will outline what the interviewee will be subject to during the interview, the fact they will remain anonymous, as well as provide contact details so they can redact provided information later if they feel the need to do so.

A similar form will also be used during the evaluation phase of the project, in which interviewees will be asked to evaluate the software produced.

Once the software is published, it will be released as open source and free to download. Due to this it will be licenced with the MIT license.

When being used in the classroom if the software fails to emulate gates correctly, students may become misinformed on the interaction or properties of logic gates. To ensure any issues which arise because of this are covered, a declaration stating that the software may not be entirely accurate will be added to the release documents.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Legal issue | Social issue | Ethical issue | Professional issue |
| Interviews |  | X | X | X |
| Software release Licencing | X | X |  | X |
| Use of the software |  | X | X | X |

## Synoptic Reflections

This section will comprise of a reflection on the project in relation to employment aspirations and the skills that you have developed towards this through engagement with the project.

ReferenceS

Vogt, C. 1999. Creating Long Documents using Microsoft Word. Published on the Web at the Nottingham Trent University.

**Note:** References are a list that includes the essential bibliographical details for each item to which you have referred in the body of your paper. It should ONLY include items to which you have made direct reference. A direct reference is where you have quoted/reproduced text or diagrams from another author or mentioned/referred to the work of another author in your report. That is quoted directly what they have said about something or mentioned their views or conclusions in your report. For details of citation and references see the information in the Project Guide.

A Bibliography is a list of published materials that you have read or consulted for general information in the preparation of your work, concerning the subject of your Project, but have not made any direct reference to in your report i.e. 'background reading'.

You should always provide a Reference List. **A Bibliography is optional but when provided it should include all items in your Reference List as well as any additional items consulted in preparation of your work.**

Bibliography

Vogt, C. 1999. Creating Long Documents using Microsoft Word. Published on the Web at the Nottingham Trent University.

Coote, H., Dobbs, B. & Jones, C. (1996). Defining databases. Wiley: Melbourne.

Applications and Science in Soft Computing, Lotfi, Ahmad; Garibaldi, Jonathon M. (Eds.) 2004, X, 346 p. Springer, ISBN: 3-540-40856-8

**Note:** A Bibliography is a list of published materials that you have read or consulted for general information in the preparation of your work, concerning the subject of your Project, but have not made any direct reference to in your report i.e. 'background reading'.

You should always provide a Reference List. **A Bibliography is optional but when provided it should include all items in your Reference List as well as any additional items consulted in preparation of your work.**

Appendix A

PUT USER DOCUMENTATION

The content of these will differ with the different types of project. Any design and analysis charts/diagrams will be included here in full. In projects where software has been developed there will be an appendix for this. Our departmental requirement is that a CD, DVD or USB memory stick of all source code is submitted to your project supervisor. The appendix contained in the report will refer to this CD, DVD, or USB memory stick, provide a directory style listing of the files submitted and instructions for rebuilding and running the software. This might be source code of programs written in high level languages (C, C++, etc) together with any pertinent files ('make' files, non-standard libraries, etc). Alternatively, or in addition, you can place some or all of the source code in the appendix. In any case the source code needed to reconstruct any software you have developed must be submitted in its entirety in the CD, DVD, or USB memory stick. (Any code that has been used from a third party should reference the original developer).

Hardware designs will require schematics/circuit diagrams, PCB layouts, simulation tests and pin outs.

Most projects will require some form of user documentation to explain how to use the software/hardware produced. A researcher following up the work may wish to utilise the work of the original author and an appendix laying out the format of input files and how to interpret the output is required.